Diego Ferraro

Embedded Systems Engineer

3 April 1992

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About me

Embedded systems engineer with eight years' experience building firmware for automotive sensor platforms. Comfortable across the stack from register-level driver code to the CI pipelines that test it.

Skill

C

Embedded Linux

CAN bus

Rust

Python

Italian (native)⋆6 English⋆5 German⋆2.5

(*)[The skill scale is from 0 (Fundamental Awareness) to 6 (Expert).]

interests

Amateur radio, motorcycle touring, and contributing to open-source driver projects for single-board computers.

education

2014-2016 M.Sc. Electronic Engineering — Politecnico di Torino
Thesis: Fault-tolerant CAN bus arbitration for automotive sensor clusters.

2011-2014 B.Sc. Electronic Engineering — Politecnico di Torino
Majoring in embedded systems.

experience

2020-present Senior Embedded Engineer — Marelli
Own firmware for a family of tyre-pressure sensor modules shipped in 6 vehicle platforms; led the migration of the driver layer from a bare-metal loop to a preemptive RTOS.

2016-2020 Firmware Engineer — Marelli
Wrote and maintained CAN bus drivers for ABS sensor nodes; built the in-house hardware-in-the-loop test rig used by three product teams.

publications

2019 Ferraro, D. *Deterministic arbitration for mixed-criticality CAN networks*. SAE Technical Paper Series.

2017 Ferraro, D., Bianchi, L. *A low-power wake-up scheme for automotive sensor nodes*. Embedded Systems Conference.

awards

2021 Marelli Engineering Excellence Award, for the RTOS migration project.

other information

Comfortable working directly with hardware teams and interpreting datasheets and oscilloscope traces, not just writing to a fixed specification.